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PATENT

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Attorney Docket No.: NVID-018/00US
P000177

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of James M. VAN DYKE, et al.

Serial No.: 09/687,453 Examiner: Chen, Chongshan

Confirmation No.: 1345 Art Unit: 2172

Filed: October 13, 2000

For: **CONTROLLER FOR A MEMORY SYSTEM HAVING MULTIPLE PARTITIONS**

Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

This amendment is responsive to the Official Action dated October 24, 2002. The period for response is extended with a petition filed herewith to expire on March 24, 2003. Please amend this patent application in the following manner.

IN THE CLAIMS:

Please cancel claims 1-31 without prejudice or disclaimer of the subject matter contained therein.

Please add the following new Claims.

32. (New) A graphics system, comprising:
- a graphics memory;
 - a graphics memory access bus connected to said graphics memory;
 - a plurality of graphics processing units; and
 - a memory controller connected between said graphics memory access bus and said plurality of graphics processing units, said memory controller providing a non-partitioned view of said graphics memory to said plurality of graphics processing units, while dividing said

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